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defined a split mask value

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## Patents

Patents **1 - 10** on **defined a split mask value**. (0.06 seconds)

## ▸ List view

[Cover view](#)[Generalized configurator using a declaratively constructed two-level bi ...](#)

US Pat. 5630025 - Filed Jul 13, 1994 - UNISYS Corporation

The load process will validate that no Items (**Mask** ^^ <Label-name> that are ...  
(if the **value** is NIL, no proposals will be this Item must be **split** between ...

## ▸ Sort by relevance

[Sort by date \(new first\)](#)[\[APPLICATION\] Security countermeasures for power analysis attacks](#)[Sort by date \(old first\)](#)

US Pat. 10825291 - Filed Apr 16, 2004

the final **mask value** further includes the step of masking the key **value** using a  
... the **split mask value** in the set of **split** key **mask** values that is **defined** ...

## ▸ Any status

[Issued patents](#)[\[APPLICATION\] Key masking for cryptographic processes](#)[Applications](#)

US Pat. 11128206 - Filed May 13, 2005 - Research In Motion Limited

... which each successively **defined** masked key **value** is comprised of **split** key  
... non-uniform initial **mask** values for application to the **split mask** values,  
...[\[APPLICATION\] Table splitting for cryptographic processes](#)

US Pat. 11368504 - Filed Mar 7, 2006 - Research In Motion Limited

Further, it will be understood that the random **value** data words and **mask** values  
... 1, the pair of **split**, masked tables 24, 26 is **defined** using the set of ...[Split and cover technique for phase shifting photolithography](#)

US Pat. 5523186 - Filed Dec 16, 1994 - International Business Machines Corporation

**Split** and Cover (SAC) Techniques Another class of techniques are the **split** and  
cover (SAC) techniques. FIG. 6 illustrates a **mask** layout ...[Color analysis based upon transformation to spherical coordinates](#)

US Pat. 5206918 - Filed Apr 3, 1991 - Kraft General Foods, Inc.

The cake with a **split** in the top. In such a histogram, the 5 current ^^p has a  
fixed ... T v <sup>TM</sup> **value** of 1 (1,1,1,1,1,1,1). This creates a **mask** of the ...[Printing apparatus and method of making mask pattern for exposure thereby](#)

US Pat. 5741079 - Filed Jan 22, 1997 - Seiko Epson Corporation, King Jim Co., Ltd.

... a predetermined time the plurality of **split** pulses, which are **defined** in a  
... is below a predetermined **value**. of making a **mask** pattern for exposure by ...[Split gate type transistor memory device](#)

US Pat. 5936883 - Filed Mar 27, 1997 - Sanyo Electric Co., Ltd.

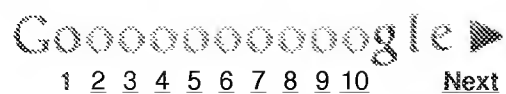
12J): The **mask** 43 is then removed, and **split** gate type memory cells 41a, ... the  
drain area 4 is to be **defined** is covered and that the edges of the **mask** 42 ...[Packet processing system including a policy engine having a classification unit](#)

US Pat. 6157955 - Filed Jun 15, 1998 - Intel Corporation

then the address PC is added to the **value** on bus REGB[9:0] to generate the ...  
The selected data is then sent into the **split**-add-**mask**-and-rotate unit 1240. ...[Method for manufacturing a split game type transistor](#)

US Pat. 6184088 - Filed Feb 25, 1999 - Sanyo Electric Co., Ltd.

In this process, the location of the 55 source area 3 is **defined** by the edges  
... 12J): The **mask** 43 is then removed, and **split** gate type memory cells 41a, ...



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